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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Howard Lee Tigelaar

Docket No: TI-35614

Serial No: 10/650,104

Conf. No: 6426

Examiner: Kevin V. Quinto

Art Unit: 2826

Filed: 08/26/2003

For: INTEGRATED CIRCUIT MOS TRANSISTOR WITH REDUCED DRAIN AND SOURCE RESISTANCE (As amended)

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 2-25-05.


Ann Trent

Dear Sir:

Responsive to the Office Action mailed December 3, 2004, in connection with the above identified application, Applicant respectfully submits the following amendments and remarks.